

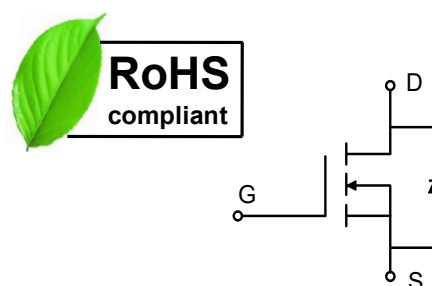
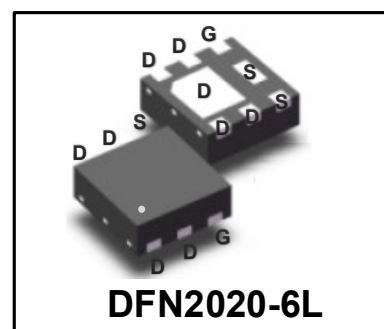
60V N-Channel Enhancement Mode Power MOSFET

Description

WMR07N06TS uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- $V_{DS} = 60V$, $I_D = 7A$
- $R_{DS(on)} < 33m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 42m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- 100% EAS Guaranteed
- Optimized for High Speed Smooth Switching



Applications

- Synchronous Rectification
- DC/DC Converter

Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ¹ (Silicon Limited)	$T_A = 25^\circ C$	I_D	7	A
	$T_A = 100^\circ C$		4.4	
Pulsed Drain Current ²		I_{DM}	28	A
Single Pulse Avalanche Energy ³		EAS	25	mJ
Total Power Dissipation ⁴	$T_A = 25^\circ C$	P_D	2.7	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	$R_{\theta JA}$	46.3	$^\circ C/W$

Electrical Characteristics $T_c = 25^\circ\text{C}$, unless otherwise noted

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	60	-	-	V
Gate-Body Leakage Current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V	-	-	1	μA
	T _J =100°C			-	-	100	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.2	1.7	2.5	V
Drain-Source on-Resistance ²		R _{DS(on)}	V _{GS} = 10V, I _D = 7A	-	25	33	mΩ
			V _{GS} = 4.5V, I _D = 5A	-	32	42	
Forward Transconductance ²		g _{fs}	V _{DS} = 5V, I _D = 7A	-	15.5	-	S
Dynamic Characteristics							
Input Capacitance		C _{iss}	V _{DS} = 30V, V _{GS} =0V, f =1MHz	-	1325	-	pF
Output Capacitance		C _{oss}		-	59	-	
Reverse Transfer Capacitance		C _{rss}		-	49	-	
Switching Characteristics							
Gate Resistance		R _G	V _{DS} = 0V, V _{GS} =0V, f =1MHz	-	1.2	-	Ω
Total Gate Charge		Q _g	V _{GS} = 10V, V _{DD} = 30V, I _D = 7A	-	22	-	nC
Gate-Source Charge		Q _{gs}		-	4.2	-	
Gate-Drain Charge		Q _{gd}		-	6.9	-	
Turn-on Delay Time		t _{d(on)}	V _{GS} =10V, V _{DD} = 30V, R _G = 3Ω, I _D = 7A	-	6.4	-	ns
Rise Time		t _r		-	15.3	-	
Turn-off Delay Time		t _{d(off)}		-	25	-	
Fall Time		t _f		-	7.6	-	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ²		V _{SD}	I _S = 7A, V _{GS} = 0V	-	-	1.2	V
Continuous Source Current ^{1,5}		I _S	V _G =V _D =0V , Force Current	-	-	7	A
Reverse Recovery Time		t _{rr}	V _R =30V, I _F =7A, dI _F /dt=100A/μs	-	26	-	ns
Reverse Recovery Charge		Q _{rr}		-	45	-	nC

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is $V_{DD} = 25V, V_{GS} = 10V, L = 0.5mH, I_{AS} = 10A$
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

Typical Characteristics

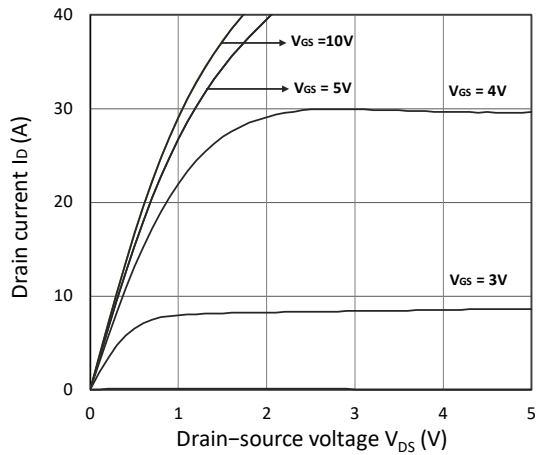


Figure 1. Output Characteristics

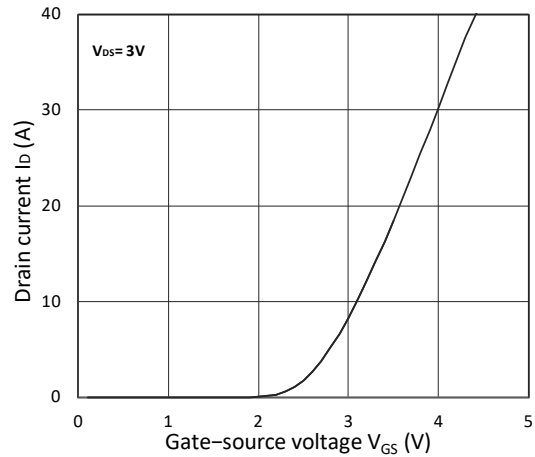


Figure 2. Transfer Characteristics

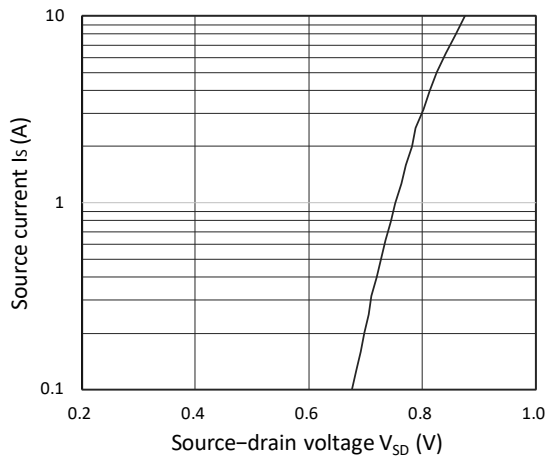
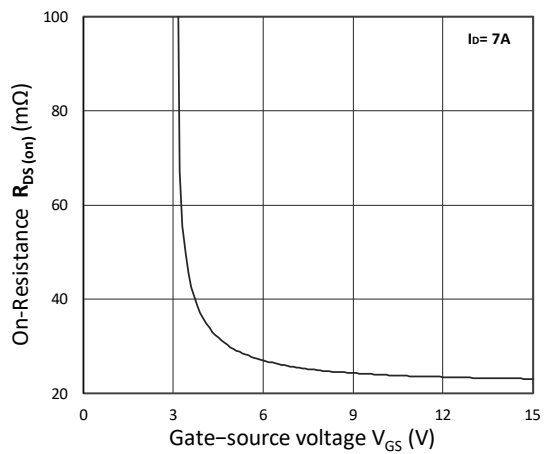
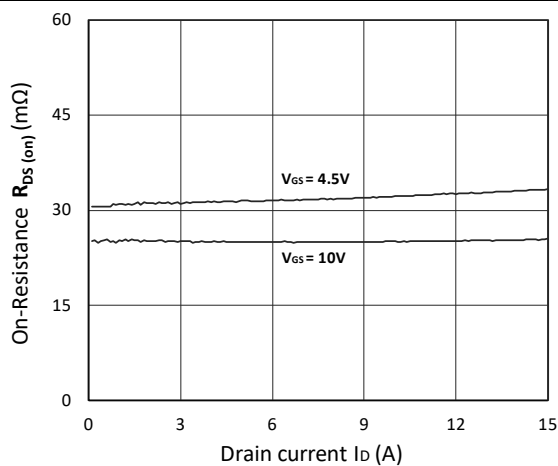
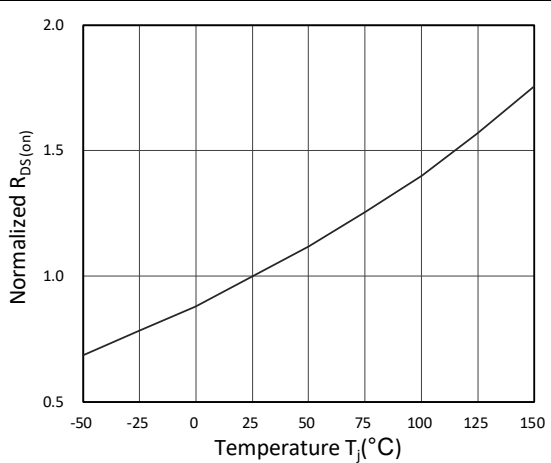


Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{DS(on)}$ vs. V_{GS} Figure 5. $R_{DS(on)}$ vs. I_D Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

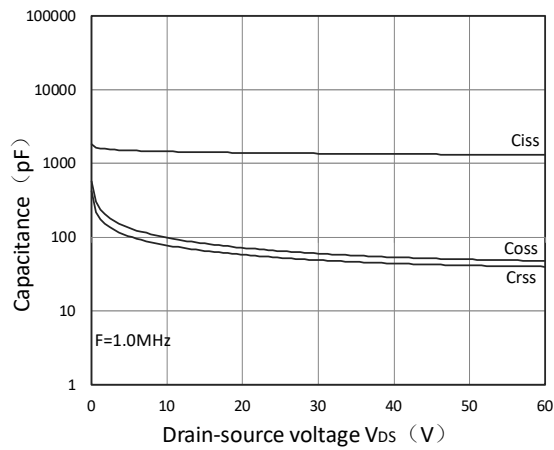


Figure 7. Capacitance Characteristics

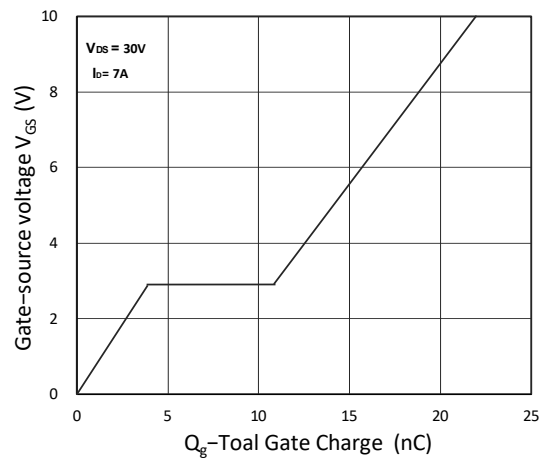


Figure 8. Gate Charge Characteristics

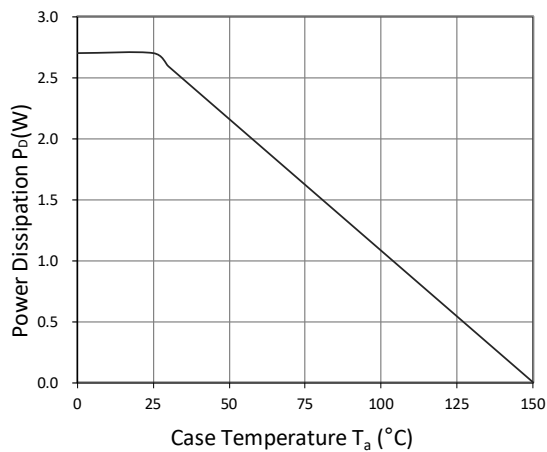


Figure 9. Power Dissipation

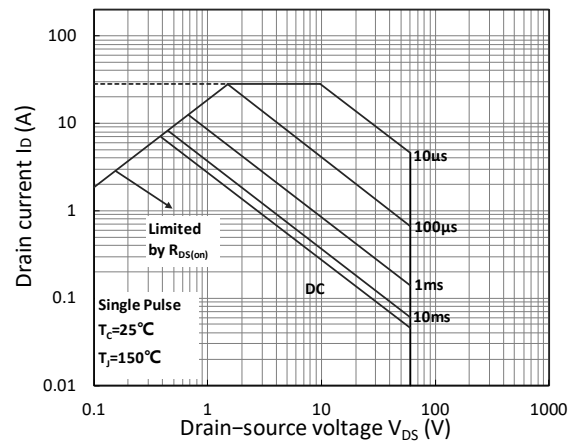


Figure 10. Safe Operating Area

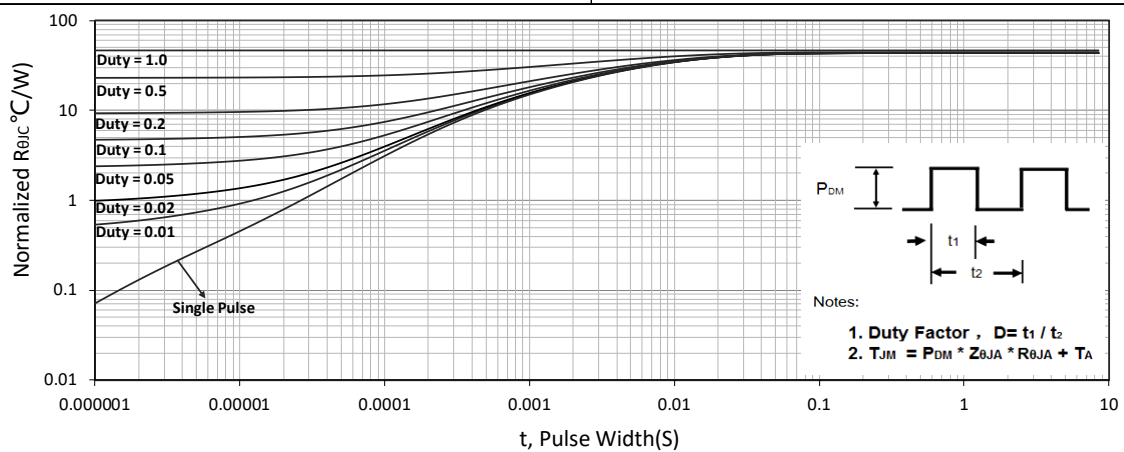


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

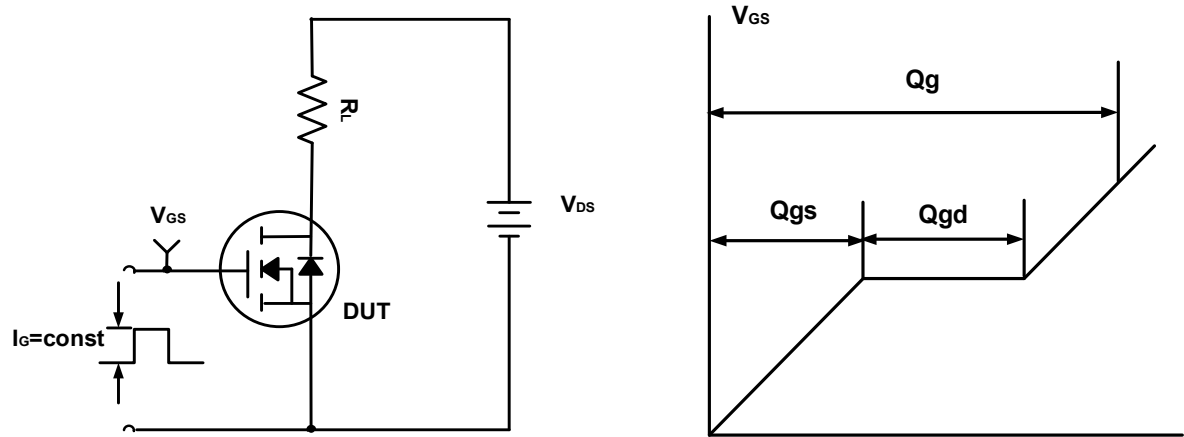


Figure A. Gate Charge Test Circuit & Waveforms

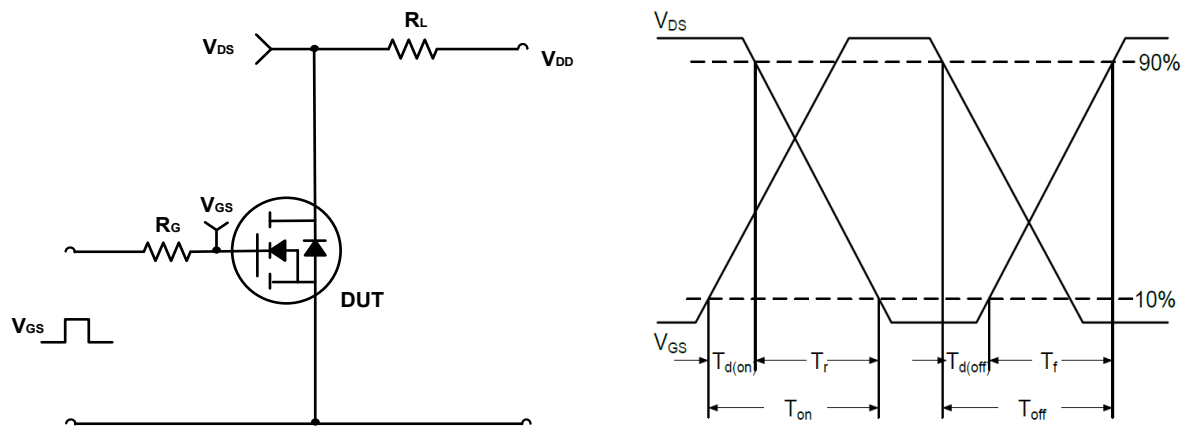


Figure B. Switching Test Circuit & Waveforms

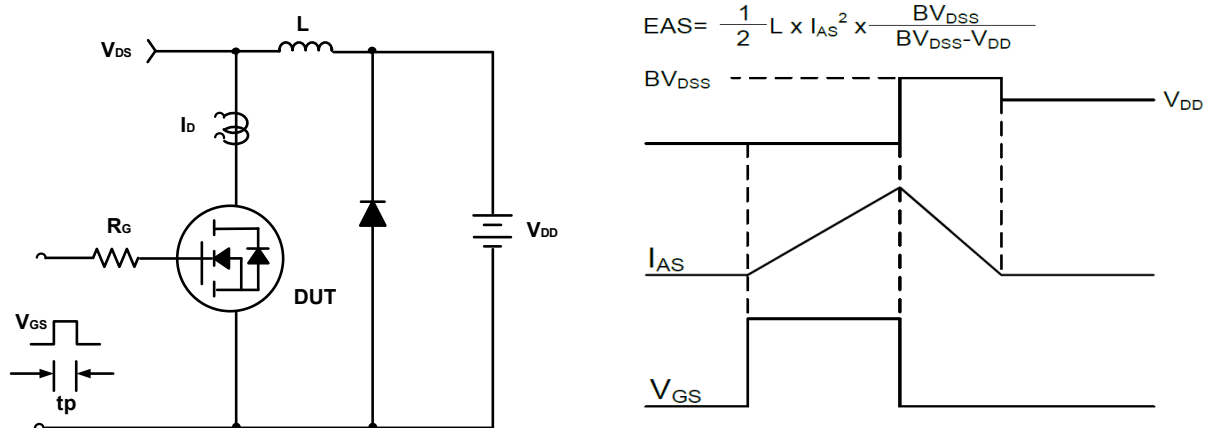
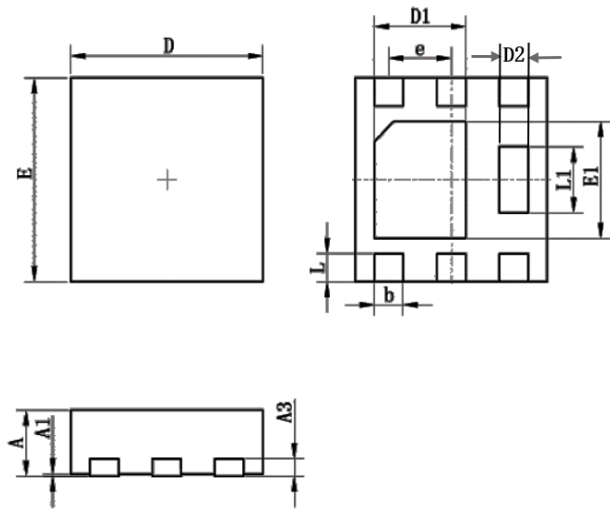


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for DFN2020-6L



COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.50	0.60
A1	0.00	0.05
A2	0.152REF	
b	0.25	0.35
D	1.95	2.05
D1	0.80	1.00
E	1.95	2.05
E1	0.80	1.00
L1	0.46	0.66
D2	0.25	0.35
e	0.65BSC	
L	0.25	0.35

Ordering Information

Part	Package	Marking	Packing method
WMR07N06TS	DFN2020-6L	R07N06	Tape and Reel

Marking Information

R07N06 = Device code

WWXXXXXX= Date code

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

WAYON® is registered trademarks of Wayon Corporation.**Disclaimer**

WAYON reserves the right to make changes without further notice to any Products herein to improve reliability, function, or design. The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. WAYON does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Products or technical information described in this document.